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J.C. PATENTS
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CERTIFICATE OF TRANSMISSION

February 1, 2005

Atty Docket No. :	JCLA8533-D2
Appl. No. :	10/755,042
Filing Date :	January, 09, 2004
Pages :	Cover + 15

BY FACSIMILE ONLY

Fax No. :	703-872-9306
Attention :	EXAMINER : MITCHELL, JAMES M.
Group Unit :	2827
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith are: <input checked="" type="checkbox"/> Transmittal sheet. <input checked="" type="checkbox"/> Amendment in 14 pages.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on February 1, 2005 at the above indicated fax number.

Sign by: 
Michelle Chang

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Docket No. JCLA8533

P5

PATENT

IN THE UNITED STATE PATENT AND TRADEMARK OFFICE

In re application of : MOU-SHIUNG LIN et al.
 Applicant : 10/755,042
 Application No. : January 09, 2004
 Filed : INTEGRATED CHIP PACKAGE STRUCTURE USING SILICON
 For : MITCHELL, JAMES M.
 Examiner :
 Art Unit : 2827

TRANSMITTAL SHEET

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in FOURTEEN (14) pages.

The fee has been calculated as shown below:

CLAIMS REMAINING AFTER AMENDMENT	CLAIMS AS FILED				ADDITIONAL FEE
	HIGHEST PREVIOUSLY PAID FOR	NO.	PRESENT EXTRA	RATE (Small Entity)	
Total Claims 47	MINUS	27	= 20 x	\$ 25	= \$ 500
Independent Claims 2	MINUS	3	= 0 x	\$ 100	= \$ 0
[] An extension of time to respond for ____ month(s) is hereby requested.					= \$ 0
					<u>TOTAL:</u> <u>\$ 500</u>

(X) The commissioner is authorized to charge the required fee in the amount of \$ 500 as calculated above or any other necessary fees in connection with the filing of this paper to Deposit Account No. 50-0710 (Order No. JCLA8533-D2).

Date: 2/11/2005

 Jiawei Huang
 Registration No. 43,330

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FEB 01 2005

Atty Docket No. JCLA8533-D2

Serial No. 10/755,042

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of :)
Mou-Shiung Lin) Examiner : Jesse A. Fenty
Serial No. : 10/755,042) Art Unit : 2815
Filed : 01/09/2004) Docket No. : JCLA8533-D2
For : INTEGRATED CHIP PACKAGE)
STRUCTURE USING SILICON)
SUBSTRATE AND METHOD OF)
MANUFACTURING THE SAME)

AMENDMENT AND RESPONSE TO OFFICE ACTION

MAIL STOP Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Office Action mailed December 16, 2004 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.